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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2133  
Examiner: To be assigned

In Re PATENT APPLICATION Of:

Applicant(s) : Hiromitsu MIYAMOTO

Serial No. : 10/606,923

Filed : June 27, 2003

For : Synchronization error detection circuit

Attorney Ref: MAE 288

**STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

Steven M. Rabin  
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January 12, 2005  
Date

SMR:dj

**FEE ENCLOSED:\$ ?**  
**Please charge any further**  
**fee to our Deposit Account**  
**No. 12-0002**